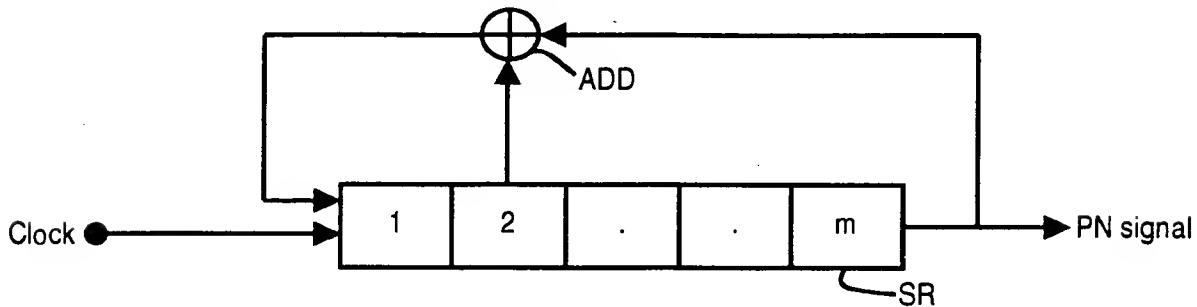


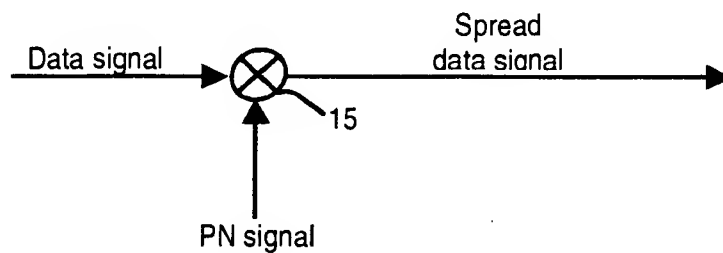
FIG. 1

Production of the spread data signal in the microcontroller itself:

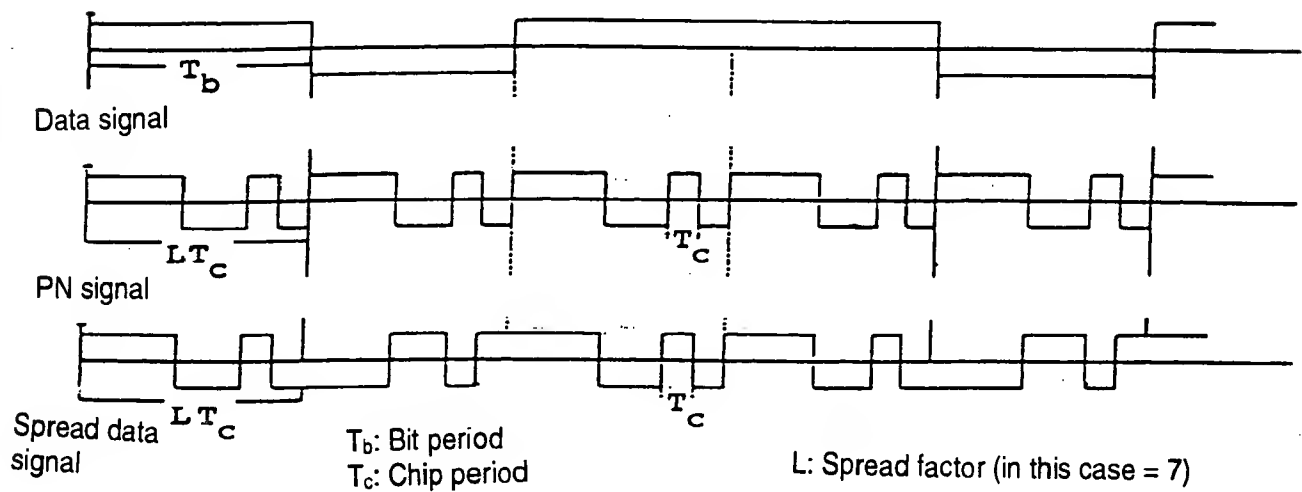
1. Production of the PN sequence by means of a feedback shift register



2. XOR linking with the data signal



Signal profile (for $m=3$):



BPSK (biphase shift keying) modulation of the carrier with a spread data signal

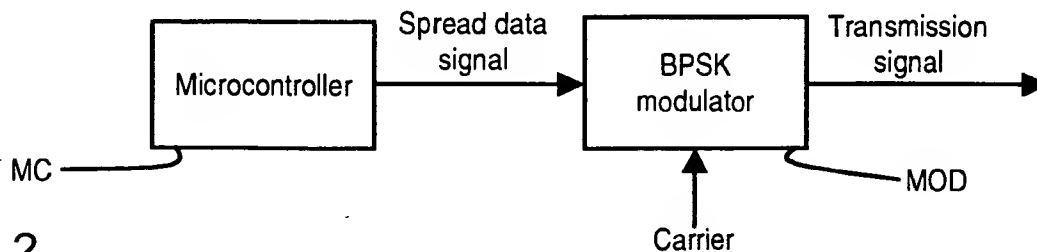


FIG. 2

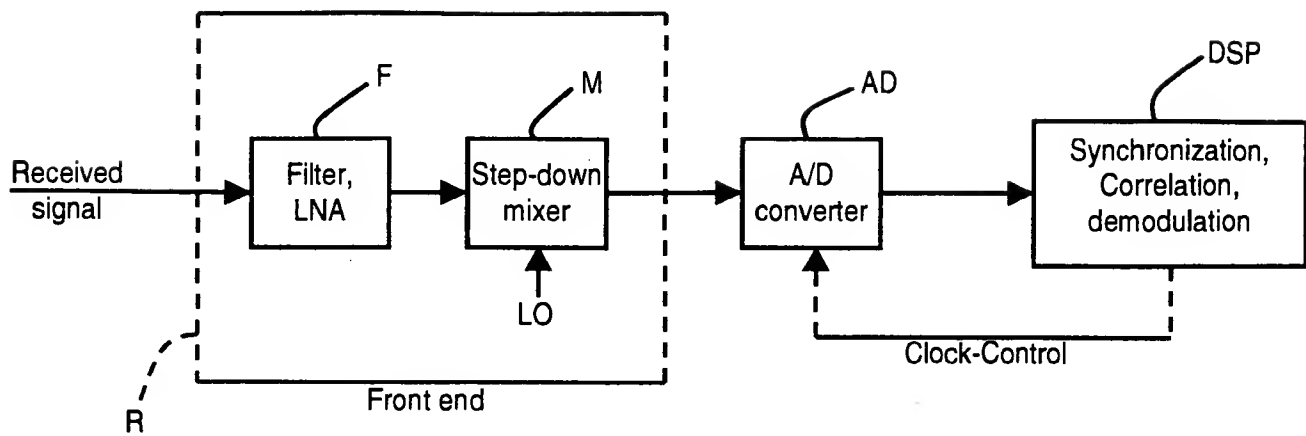


FIG. 3

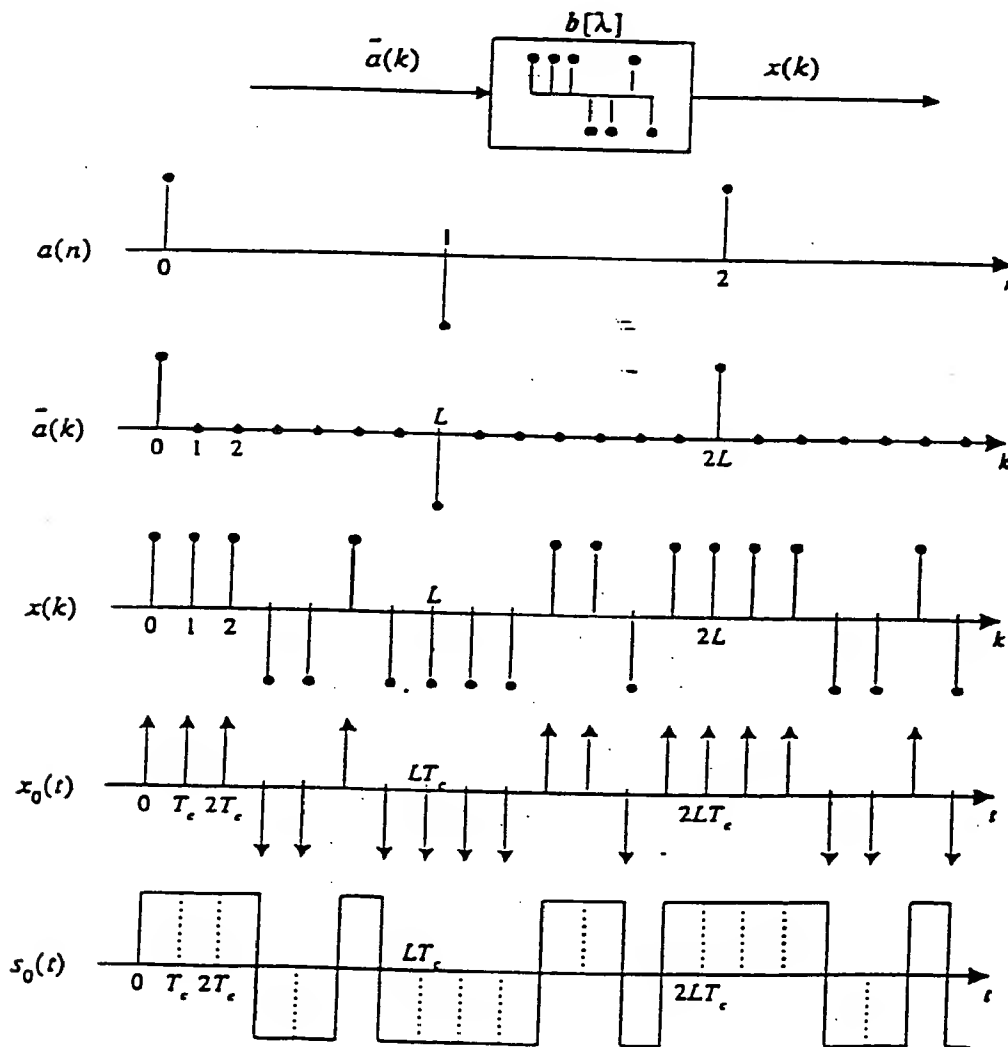


FIG. 4

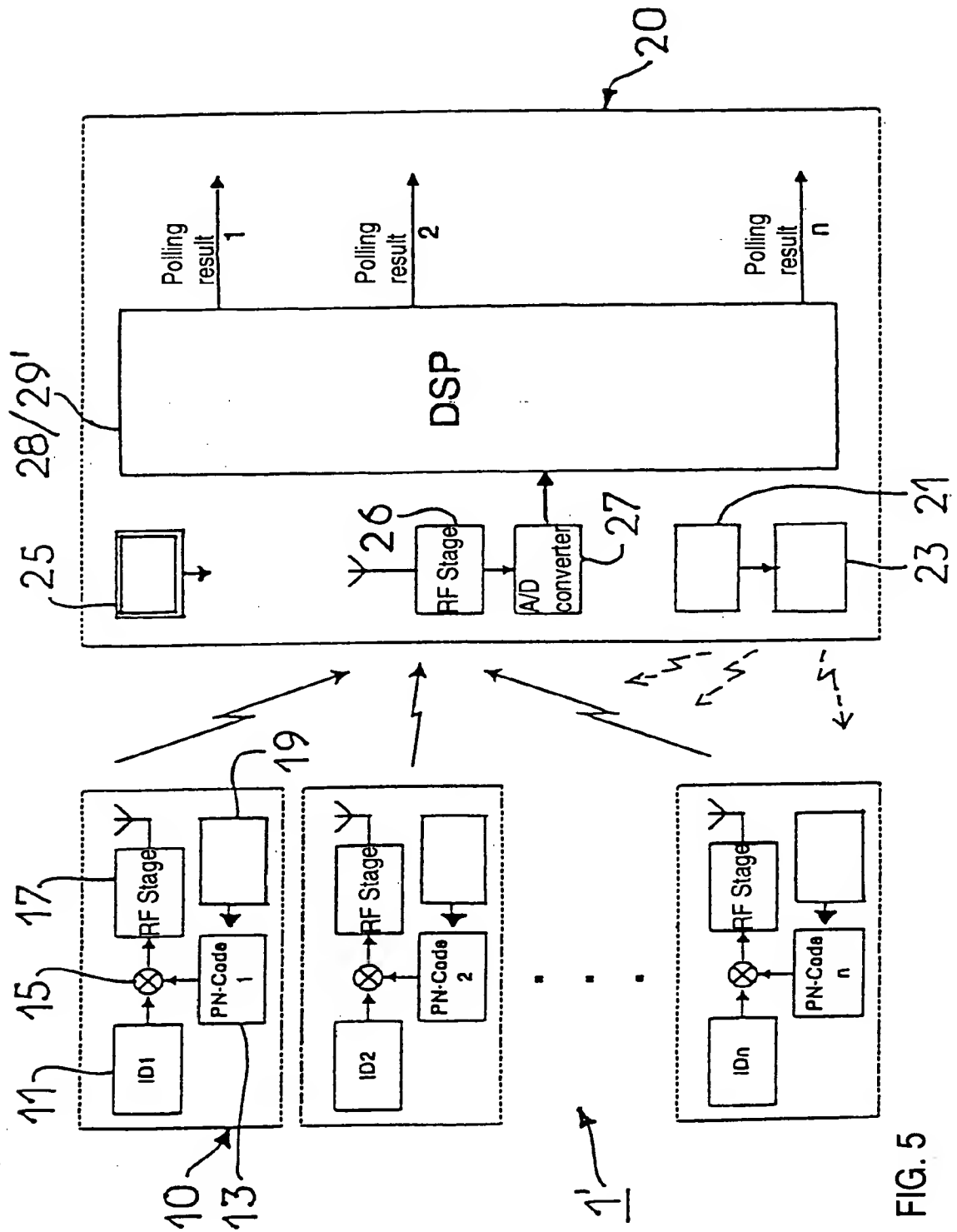


FIG. 5